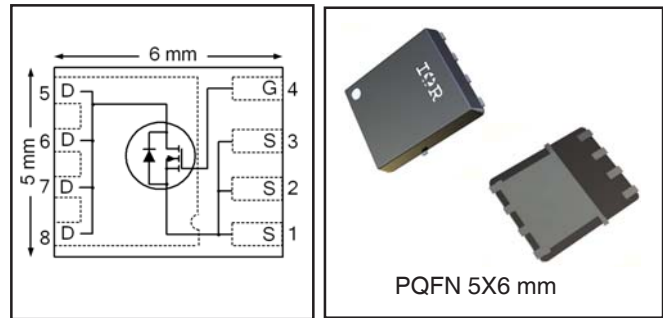


|  |             |           |
|--|-------------|-----------|
| $V_{DS}$                                   | <b>30</b>   | <b>V</b>  |
| $V_{GS\ max}$                              | <b>± 20</b> | <b>V</b>  |
| $R_{DS(on)\ max}$<br>(@ $V_{GS} = 10V$ )   | <b>2.1</b>  | <b>mΩ</b> |
| (@ $V_{GS} = 4.5V$ )                       | <b>3.2</b>  |           |
| $Q_g\ typ.$                                | <b>30</b>   | <b>nC</b> |
| $I_D$<br>(@ $T_{c(Bottom)} = 25^\circ C$ ) | <b>80</b> ⑦ | <b>A</b>  |

HEXFET® Power MOSFET



**Applications**

- Synchronous MOSFET for high frequency buck converters

**Features and Benefits**

**Features**

|  |
|--|
| Low Thermal Resistance to PCB (< 1.3°C/W)                    |
| Low Profile (<1.2mm)   |
| Industry-Standard Pinout                                     |
| Compatible with Existing Surface Mount Techniques            |
| RoHS Compliant Containing no Lead, no Bromide and no Halogen |
| MSL1, Industrial Qualification                               |

results in  
⇒

**Benefits**

|                                   |
|-----------------------------------|
| Enable better thermal dissipation |
| Increased Power Density           |
| Multi-Vendor Compatibility        |
| Easier Manufacturing              |
| Environmentally Friendlier        |
| Increased Reliability             |

| Base part number | Package Type   | Standard Pack |          | Orderable part number |
|------------------|----------------|---------------|----------|-----------------------|
|                  |                | Form          | Quantity |                       |
| IRFH8311TRPBF    | PQFN 5mm x 6mm | Tape and Reel | 4000     | IRFH8311TRPBF         |
| IRFH8311TR2PBF   | PQFN 5mm x 6mm | Tape and Reel | 400      | IRFH8311TR2PBF        |

**Absolute Maximum Ratings**

|                                     | Parameter  | Max.         | Units |
|-------------------------------------|--|--------------|-------|
| $V_{DS}$                            | Drain-to-Source Voltage                                    | 30           | V     |
| $V_{GS}$                            | Gate-to-Source Voltage                                     | ± 20         |       |
| $I_D @ T_A = 25^\circ C$            | Continuous Drain Current, $V_{GS} @ 10V$                   | 32           | A     |
| $I_D @ T_A = 70^\circ C$            | Continuous Drain Current, $V_{GS} @ 10V$                   | 26           |       |
| $I_D @ T_{C(Bottom)} = 25^\circ C$  | Continuous Drain Current, $V_{GS} @ 10V$                   | 169⑥⑦        |       |
| $I_D @ T_{C(Bottom)} = 100^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V$                   | 107⑥⑦        |       |
| $I_D @ T_C = 25^\circ C$            | Continuous Drain Current, $V_{GS} @ 10V$ (Package Limited) | 80⑦          |       |
| $I_{DM}$                            | Pulsed Drain Current ①                                     | 400          | W     |
| $P_D @ T_A = 25^\circ C$            | Power Dissipation ⑤  | 3.6          |       |
| $P_D @ T_{C(Bottom)} = 25^\circ C$  | Power Dissipation ⑤  | 96           |       |
|                                     | Linear Derating Factor ⑤                                   | 0.029        | W/°C  |
| $T_J$<br>$T_{STG}$                  | Operating Junction and<br>Storage Temperature Range        | -55 to + 150 | °C    |

Notes ① through ⑦ are on page 9

**Static @ T<sub>J</sub> = 25°C (unless otherwise specified)**

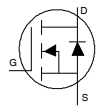
|  | Parameter   | Min. | Typ.  | Max. | Units | Conditions   |
|--|---|------|-------|------|-------|--|
| B <sub>V<sub>DSS</sub></sub>                   | Drain-to-Source Breakdown Voltage                   | 30   | —     | —    | V     | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA   |
| ΔB <sub>V<sub>DSS</sub></sub> /ΔT <sub>J</sub> | Breakdown Voltage Temp. Coefficient                 | —    | 0.021 | —    | V/°C  | Reference to 25°C, I <sub>D</sub> = 1.0mA  |
| R <sub>DS(on)</sub>                            | Static Drain-to-Source On-Resistance                | —    | 1.70  | 2.10 | mΩ    | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A ③  |
|  |   | —    | 2.60  | 3.20 |       | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 16A ③   |
| V <sub>GS(th)</sub>                            | Gate Threshold Voltage                              | 1.35 | 1.8   | 2.35 | V     | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 100μA                                     |
| ΔV <sub>GS(th)</sub>                           | Gate Threshold Voltage Coefficient                  | —    | -6.6  | —    | mV/°C |  |
| I <sub>DSS</sub>                               | Drain-to-Source Leakage Current                     | —    | —     | 1    | μA    | V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V  |
|  |   | —    | —     | 150  |       | V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C                            |
| I <sub>GSS</sub>                               | Gate-to-Source Forward Leakage                      | —    | —     | 100  | nA    | V <sub>GS</sub> = 20V  |
|  | Gate-to-Source Reverse Leakage                      | —    | —     | -100 |       | V <sub>GS</sub> = -20V   |
| g <sub>fs</sub>                                | Forward Transconductance                            | 83   | —     | —    | S     | V <sub>DS</sub> = 10V, I <sub>D</sub> = 20A  |
| Q <sub>g</sub>                                 | Total Gate Charge                                   | —    | 66    | —    | nC    | V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A                             |
| Q <sub>g</sub>                                 | Total Gate Charge                                   | —    | 30    | —    | nC    | V <sub>DS</sub> = 15V<br>V <sub>GS</sub> = 4.5V<br>I <sub>D</sub> = 20A                        |
| Q <sub>gs1</sub>                               | Pre-V <sub>th</sub> Gate-to-Source Charge           | —    | 9.7   | —    |       |  |
| Q <sub>gs2</sub>                               | Post-V <sub>th</sub> Gate-to-Source Charge          | —    | 3.9   | —    |       |  |
| Q <sub>gd</sub>                                | Gate-to-Drain Charge                                | —    | 8.6   | —    |       |  |
| Q <sub>gqdr</sub>                              | Gate Charge Overdrive                               | —    | 7.8   | —    |       |  |
| Q <sub>sw</sub>                                | Switch Charge (Q <sub>gs2</sub> + Q <sub>gd</sub> ) | —    | 12.5  | —    |       |  |
| Q <sub>oss</sub>                               | Output Charge                                       | —    | 21    | —    | nC    | V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V  |
| R <sub>G</sub>                                 | Gate Resistance                                     | —    | 0.6   | —    | Ω     |  |
| t <sub>d(on)</sub>                             | Turn-On Delay Time                                  | —    | 21    | —    | ns    | V <sub>DD</sub> = 15V, V <sub>GS</sub> = 4.5V<br>I <sub>D</sub> = 20A<br>R <sub>G</sub> = 1.8Ω |
| t <sub>r</sub>                                 | Rise Time   | —    | 26    | —    |       |  |
| t <sub>d(off)</sub>                            | Turn-Off Delay Time                                 | —    | 21    | —    |       |  |
| t <sub>f</sub>                                 | Fall Time   | —    | 12    | —    |       |  |
| C <sub>iss</sub>                               | Input Capacitance                                   | —    | 4960  | —    | pF    | V <sub>GS</sub> = 0V<br>V <sub>DS</sub> = 10V<br>f = 1.0MHz                                    |
| C <sub>oss</sub>                               | Output Capacitance                                  | —    | 1065  | —    |       |  |
| C <sub>rss</sub>                               | Reverse Transfer Capacitance                        | —    | 455   | —    |       |  |

**Avalanche Characteristics**

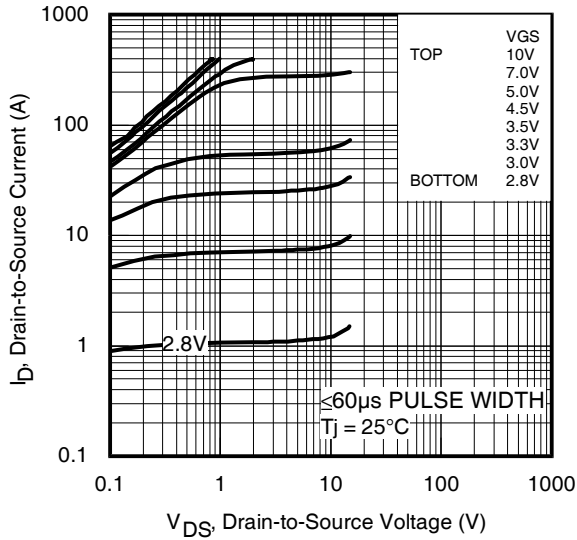
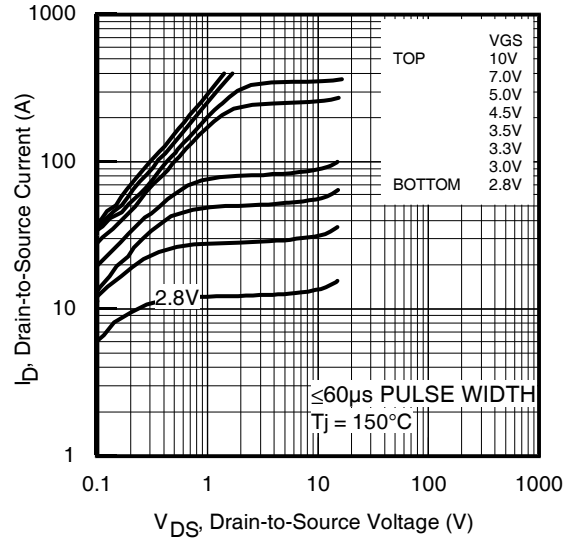
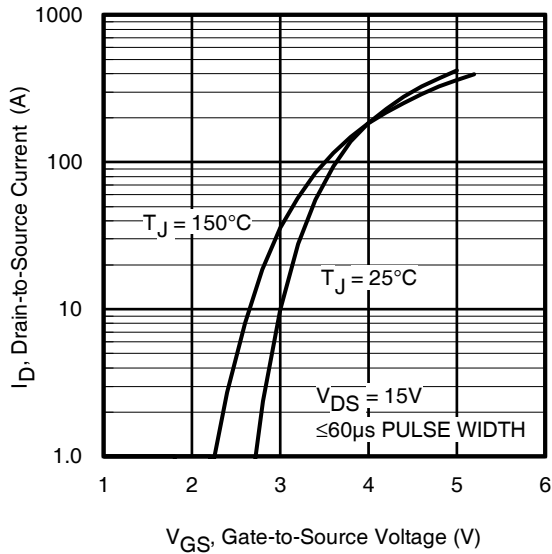
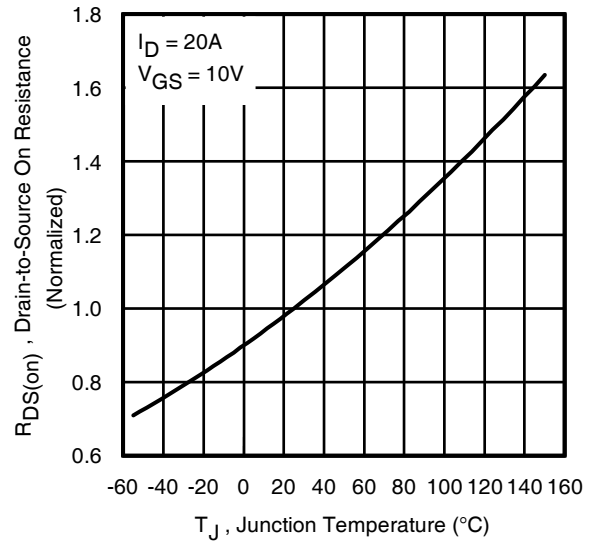
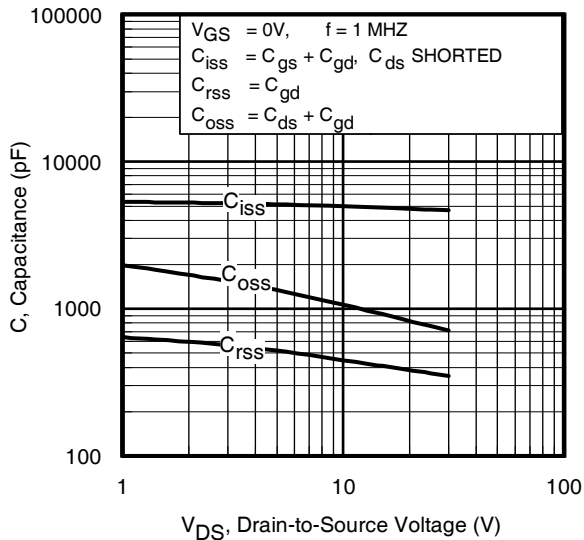
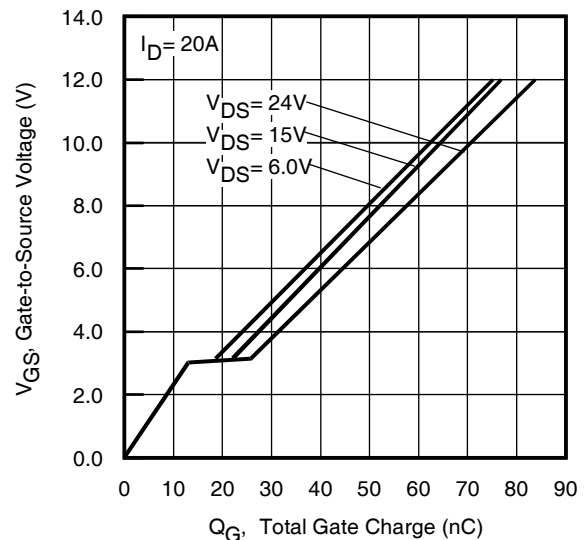
|                 | Parameter                       | Typ. | Max. | Units |
|-----------------|---------------------------------|------|------|-------|
| E <sub>AS</sub> | Single Pulse Avalanche Energy ② | —    | 326  | mJ    |
| I <sub>AR</sub> | Avalanche Current ①             | —    | 20   | A     |

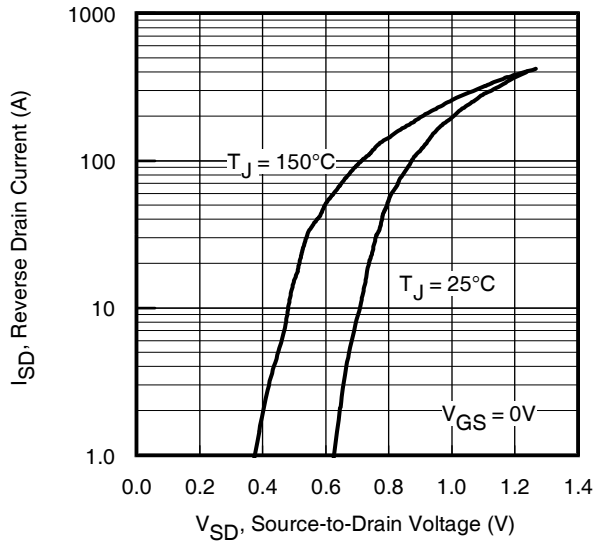
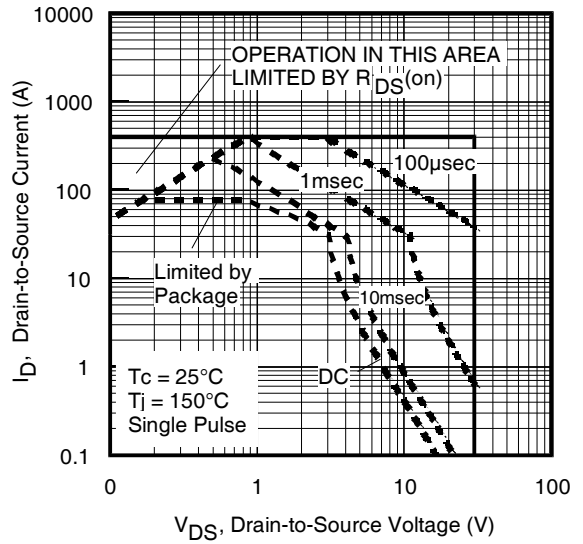
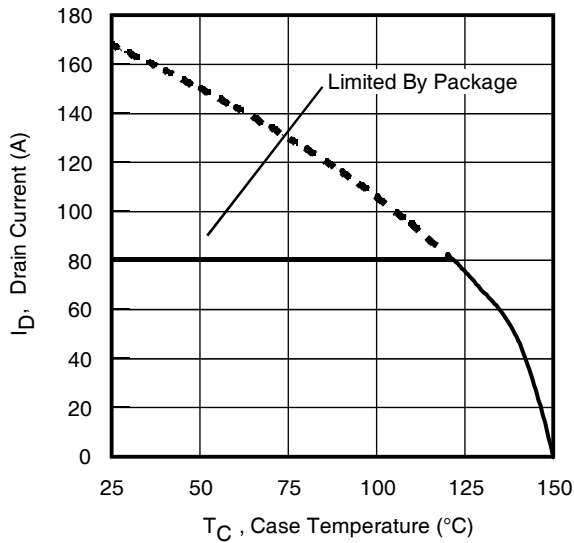
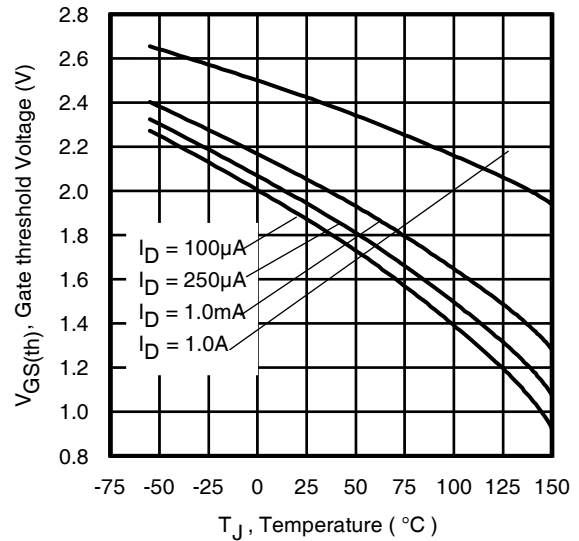
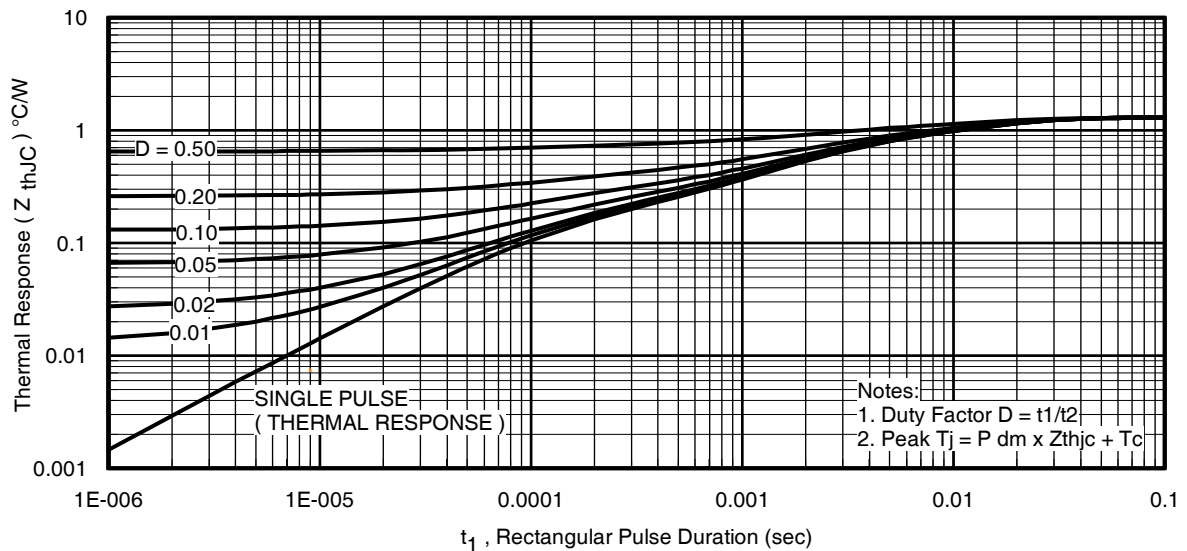
**Diode Characteristics**

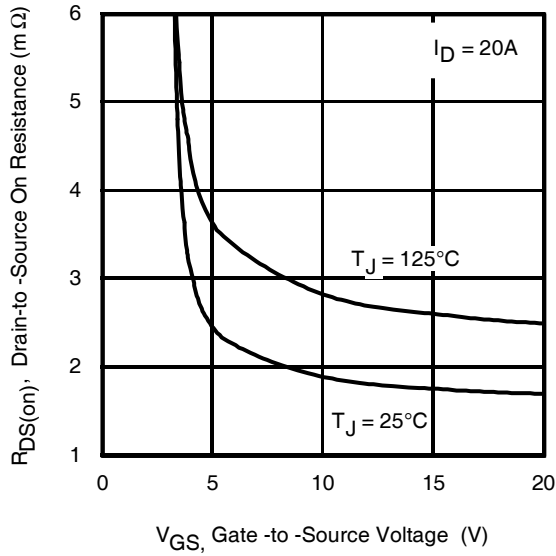
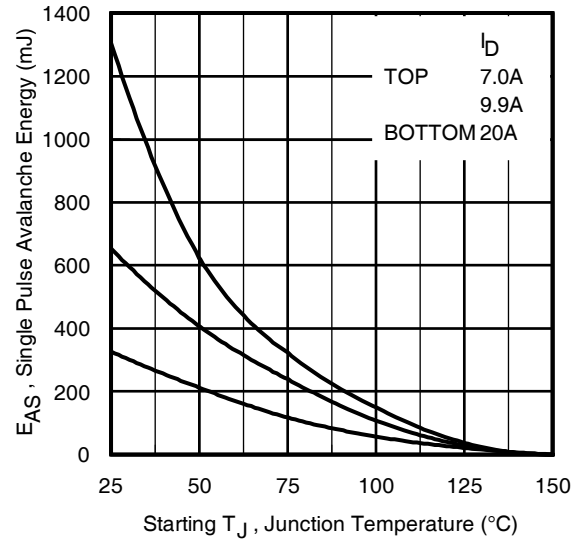
|                 | Parameter                                 | Min.                                      | Typ. | Max. | Units | Conditions  |
|-----------------|---|---|------|------|-------|---|
| I <sub>S</sub>  | Continuous Source Current<br>(Body Diode) | —   | —    | 80 ② | A     | MOSFET symbol<br>showing the<br>integral reverse<br>p-n junction diode. |
| I <sub>SM</sub> | Pulsed Source Current<br>(Body Diode) ①   | —   | —    | 400  |       |   |
| V <sub>SD</sub> | Diode Forward Voltage                     | —   | —    | 1.0  | V     | T <sub>J</sub> = 25°C, I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V ③     |
| t <sub>rr</sub> | Reverse Recovery Time                     | —   | 22   | 33   | ns    | T <sub>J</sub> = 25°C, I <sub>F</sub> = 20A, V <sub>DD</sub> = 15V      |
| Q <sub>rr</sub> | Reverse Recovery Charge                   | —   | 47   | 71   | nC    | di/dt = 390 A/μs ③  |
| t <sub>on</sub> | Forward Turn-On Time                      | Time is dominated by parasitic Inductance |      |      |       |   |


**Thermal Resistance**

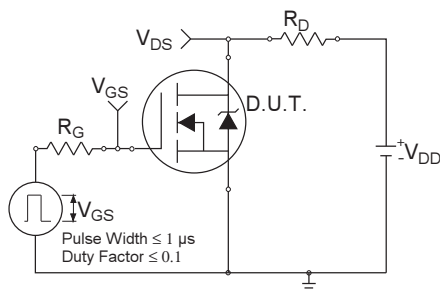
|                           | Parameter             | Typ. | Max. | Units |
|---------------------------|-----------------------|------|------|-------|
| R <sub>θJC</sub> (Bottom) | Junction-to-Case ④    | —    | 1.3  | °C/W  |
| R <sub>θJC</sub> (Top)    | Junction-to-Case ④    | —    | 30   |       |
| R <sub>θJA</sub>          | Junction-to-Ambient ⑤ | —    | 35   |       |
| R <sub>θJA</sub> (<10s)   | Junction-to-Ambient ⑤ | —    | 0.99 |       |


**Fig 1. Typical Output Characteristics**

**Fig 2. Typical Output Characteristics**

**Fig 3. Typical Transfer Characteristics**

**Fig 4. Normalized On-Resistance vs. Temperature**

**Fig 5. Typical Capacitance vs. Drain-to-Source Voltage**

**Fig 6. Typical Gate Charge vs. Gate-to-Source Voltage**

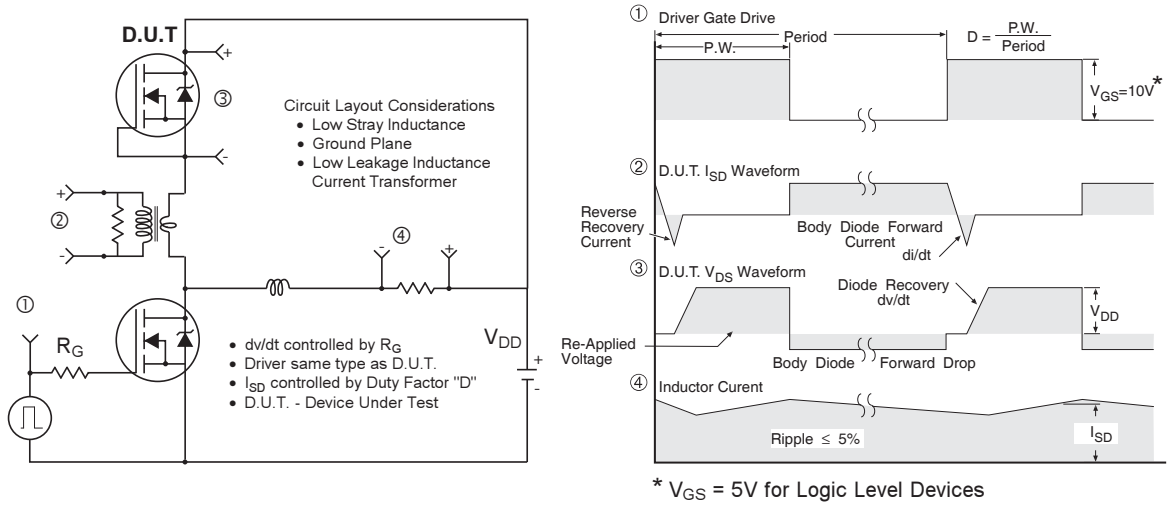

**Fig 7.** Typical Source-Drain Diode Forward Voltage

**Fig 8.** Maximum Safe Operating Area

**Fig 9.** Maximum Drain Current vs. Case (Bottom) Temperature

**Fig 10.** Threshold Voltage vs. Temperature

**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case (Bottom)


**Fig 12. On-Resistance vs. Gate Voltage**

**Fig 13. Maximum Avalanche Energy vs. Drain Current**

**Fig 14a. Unclamped Inductive Test Circuit**

**Fig 14b. Unclamped Inductive Waveforms**

**Fig 15a. Switching Time Test Circuit**

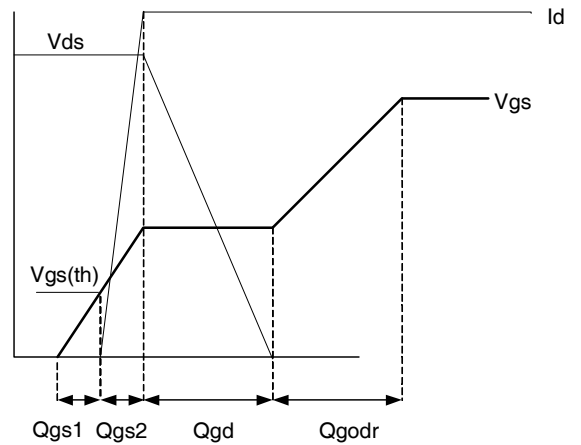
**Fig 15b. Switching Time Waveforms**



**Fig 16. Peak Diode Recovery  $dv/dt$  Test Circuit for N-Channel HEXFET<sup>®</sup> Power MOSFETs**

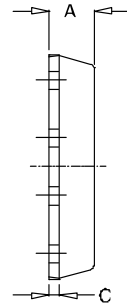


**Fig 17. Gate Charge Test Circuit**

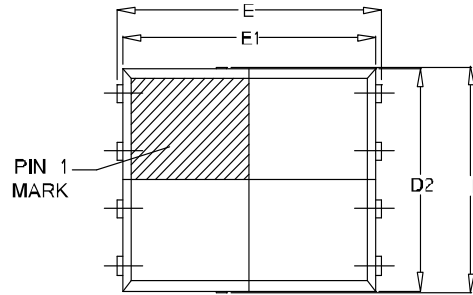


**Fig 18. Gate Charge Waveform**

## PQFN 5x6 Outline "E" Package Details

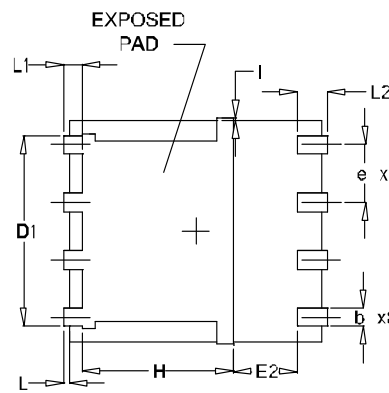


SIDEVIEW



TOP VIEW

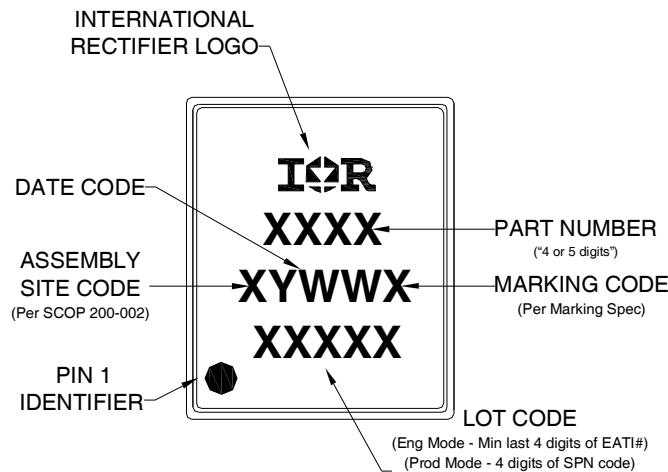
| SYMBOL | OUTLINE PQFN 5X6E |      |      |
|--------|-------------------|------|------|
|        | MIN.              | NOM. | MAX. |
| A      | 0.90              | 1.03 | 1.17 |
| b      | 0.33              | 0.41 | 0.48 |
| C      | 0.20              | 0.25 | 0.35 |
| D      | 4.80              | 4.98 | 5.15 |
| D1     | 3.91              | 4.11 | 4.31 |
| D2     | 4.80              | 4.90 | 5.00 |
| E      | 5.90              | 6.02 | 6.15 |
| E1     | 5.65              | 5.75 | 5.85 |
| E2     | 1.10              | —    | —    |
| e      | 1.27 BSC          |      |      |
| L      | 0.05              | 0.15 | 0.25 |
| L1     | 0.38              | 0.44 | 0.50 |
| L2     | 0.51              | 0.68 | 0.86 |
| H      | 3.32              | 3.45 | 3.58 |
| I      | —                 | —    | 0.18 |



BOTTOM VIEW

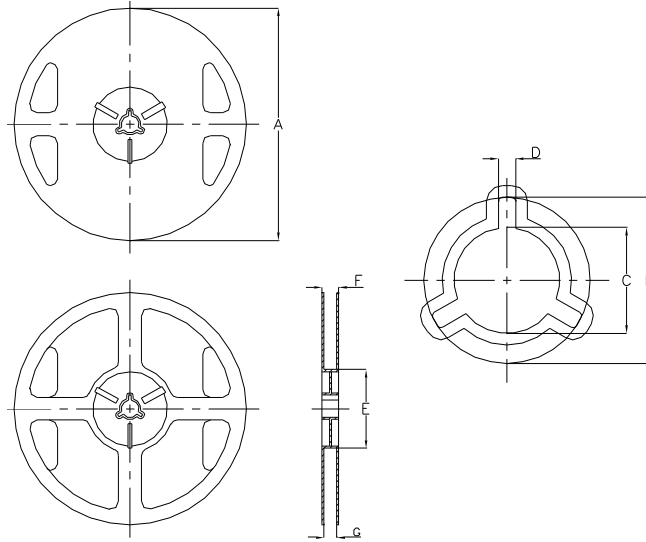
For footprint and stencil design recommendations, please refer to application note AN-1154 at <http://www.irf.com/technical-info/appnotes/an-1154.pdf>

## PQFN 5x6 Outline "E" Part Marking



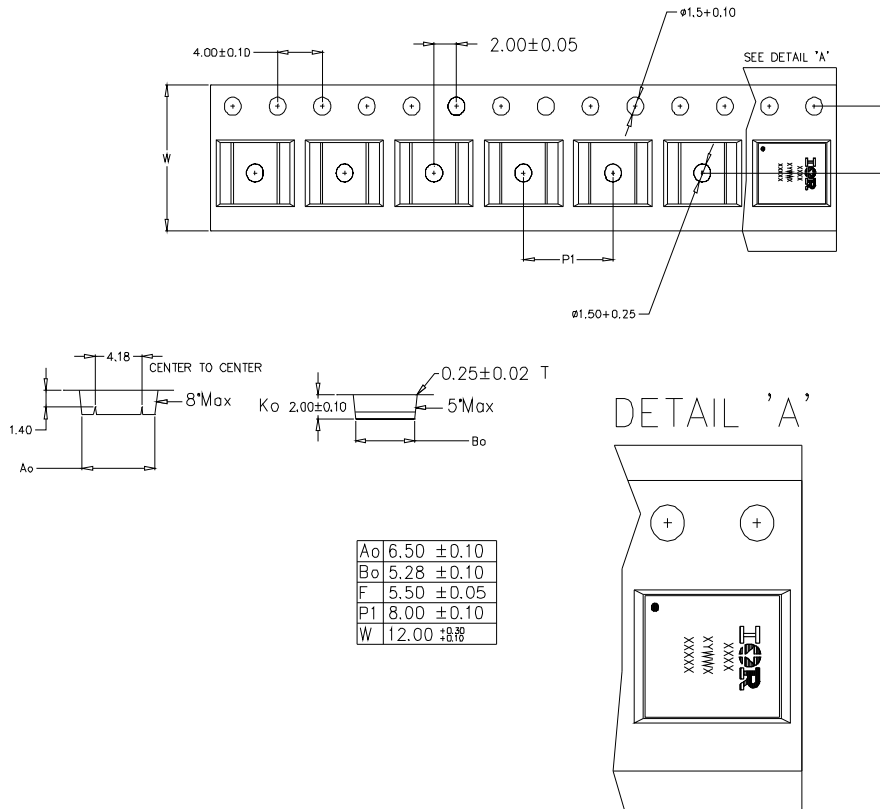
Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

# PQFN 5x6 Outline "E" Tape and Reel



NOTE: Controlling dimensions in mm Std reel quantity is 4000 parts.

| REEL DIMENSIONS |                            |       |          |        |                      |       |          |       |
|-----------------|----------------------------|-------|----------|--------|----------------------|-------|----------|-------|
| CODE            | STANDARD OPTION (QTY 4000) |       |          |        | TR2 OPTION (QTY 400) |       |          |       |
|                 | METRIC                     |       | IMPERIAL |        | METRIC               |       | IMPERIAL |       |
| A               | 329.5                      | 330.5 | 12.972   | 13.011 | 177.5                | 178.5 | 6.988    | 7.028 |
| B               | 20.9                       | 21.5  | 0.823    | 0.846  | 20.9                 | 21.5  | 0.823    | 0.846 |
| C               | 12.8                       | 13.5  | 0.504    | 0.532  | 13.2                 | 13.8  | 0.520    | 0.543 |
| D               | 1.7                        | 2.3   | 0.067    | 0.091  | 1.9                  | 2.3   | 0.075    | 0.091 |
| E               | 97                         | 99    | 3.819    | 3.898  | 65                   | 66    | 2.350    | 2.598 |
| F               | Ref                        | 17.4  |          |        | Ref                  | 12    |          |       |
| G               | 13                         | 14.5  | 0.512    | 0.571  | 13                   | 14.5  | 0.512    | 0.571 |





**Qualification information<sup>†</sup>**

|                            |   |  |
|----------------------------|---|--|
| Qualification level        | Industrial <sup>††</sup><br>(per JEDEC JESD47F <sup>†††</sup> guidelines) |  |
| Moisture Sensitivity Level | PQFN 5mm x 6mm  | MSL1<br>(per JEDEC J-STD-020D <sup>†††</sup> ) |
| RoHS compliant             | Yes   |  |

† Qualification standards can be found at International Rectifier's web site  
<http://www.irf.com/product-info/reliability>

†† Higher qualification ratings may be available should the user have such requirements.  
 Please contact your International Rectifier sales representative for further information:  
<http://www.irf.com/whoto-call/salesrep/>

††† Applicable version of JEDEC standard at the time of product release.

**Notes:**

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting  $T_J = 25^\circ\text{C}$ ,  $L = 1.63\text{mH}$ ,  $R_G = 50\Omega$ ,  $I_{AS} = 20\text{A}$ .
- ③ Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ④  $R_\theta$  is measured at  $T_J$  of approximately  $90^\circ\text{C}$ .
- ⑤ When mounted on 1 inch square 2 oz copper pad on 1.5x1.5 in. board of FR-4 material.
- ⑥ Calculated continuous current based on maximum allowable junction temperature.
- ⑦ Current is limited to 80A by source bonding technology.

**Revision History**

| Date     | Comments                                       |
|----------|--|
| 3/8/2013 | Updated package current limit from 50A to 80A. |